Jc672 U.S. PTO
09/924333
08/07/01

Class	Subclass	ISSUE CLASSIFICATION

438/197

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.
SCANNED KS QA CR

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/924333	D	~~118~~ 156		1763	~~[illegible]~~ Zervigon

APPLICANTS: Paul Shirley

TITLE: Method and apparatus for controlling a temperature of a microelectronic substrate

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL CLASS	ORIGINAL SUBCLASS	CROSS REFERENCE(S) CLASS	CROSS REFERENCE(S) SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

☒ Continued on Issue Slip inside File Jacket

☒ TERMINAL DISCLAIMER

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☒ The term of this patent shall not extend beyond the expiration date of U.S. Patent No. 6,322,681

☐ The terminal ____ months of this patent have been disclaimed.

DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.

(Assistant Examiner) (Date)

(Primary Examiner) (Date)

(Legal Instruments Examiner) (Date)

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due	Date Paid

ISSUE BATCH NUMBER

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